

IFW

PATENT

IN THE UNITED STATES PATENT OFFICE

Applicant: Eric J. Crabill  
Assignee: Xilinx, Inc.  
Title: "Configurable Processor System"

Serial No.: 10/805,116      File Date: 03-19-04  
Examiner: Unknown      Art Unit: 2819  
Docket No.: X-841-1C US      Conf. No.: 8509

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Mail Stop AMENDMENT  
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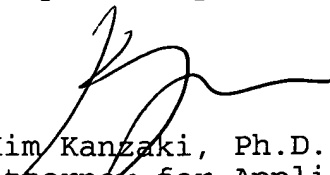
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the sixty-nine (69) references listed in the attached Substitute for Form 1449A/PTO. A copy of sixteen (16) of the references is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

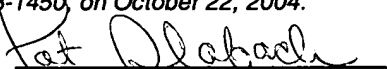
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Respectfully submitted,

  
Kim Kanzaki, Ph.D.  
Attorney for Applicants  
Reg. No. 37,652

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Pat Slaback  
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Modified 02-03

PTO/SB/21 (01-03)  
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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>  Mail Stop: <b>AMENDMENT</b>  Express Mail Receipt No.  Total Number of Pages in This Submission	Application / Conf. No.	10/805,116 / 8509
	Filing Date	March 19, 2004
	First Named Inventor	Eric J. Crabill
	Examiner Name	Unknown
	Art Unit	2819
	Patent No.	
Attorney Docket Number	X-841-1C US	

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (with Recordation Cover Sheet)	<input type="checkbox"/> After Allowance Communication to
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## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Customer Number	24309 (Customer Number)	Reg. Number 37,652
Attn: Kim Kanzaki		
Signature		
Date	October 22, 2004	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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This collection of information is required by 37 CFR 1.17 and 1.27. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 4

**Complete if Known**

Application / Conf. No.	10/805,116 / 8509
Filing Date	March 19, 2004
First Named Inventor	Eric J. Crabill
Art Unit	2819
Examiner Name	Unknown
Attorney Docket Number	X-841-1C US

**U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
		US- Re 3,363	08-03-93	Freeman	
		US- 4,314,353	02-02-82	Gunter et al.	
		US- 4,789,951	12-06-88	Birkner et al.	
		US- 4,870,302	09-26-89	Freeman	
		US- 5,093,915	03-03-92	Platteter et al.	
		US- 5,140,193	08-18-92	Freeman et al.	
		US- 5,206,940	04-27-93	Murakami et al.	
		US- 5,343,406	08-30-94	Freeman et al.	
		US- 5,347,641	09-13-94	Cedar et al.	
		US- 5,369,314	11-29-94	Patel et al.	
		US- 5,402,014	03-28-95	Ziklik et al.	
		US- 5,432,719	07-11-95	Freeman et al.	
		US- 5,432,950	07-11-95	Sibigtroth	
		US- 5,448,493	09-05-95	Topolewski et al.	
		US- 5,488,316	01-30-96	Freeman et al.	
		US- 5,504,439	04-02-96	Tavana	
		US- 5,592,102	01-07-97	Lane et al.	
		US- 5,635,851	06-03-97	Tavana	
		US- 5,682,107	10-28-97	Tavana et al.	
		US- 5,710,891	01-20-98	Normoyte et al.	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>2</sup> - Number <sup>3</sup> - Kind Code <sup>4</sup> (if known)				
		EP 0 062 431 A1	03-19-82	Fujitsu Limited		
		EP 0 139 254 A2	09-28-84	IBM		
		EP 0 307 649 A2	08-19-88	Compaq Computer Corp.		
		EP 0 306 962 A2	09-08-88	Hitachi, Ltd.		
		EP 0 361 525 A2	09-29-89	Hitachi, Ltd.		
		EP 0 486 248 A2	11-12-91	Xilinx, Inc.		
		EP 0 503 498 A2	03-06-92	Oki Electric Industry Co		
		EP 0 511 674 A2	04-30-92	Kabushiki Kaisha Toshiba		
		EP 0 536 793 A2	10-09-92	Kabushiki Kaisha Toshiba		
		EP 0 636 976 A1	07-21-94	Philips Electronics N.V.		

Examiner  
SignatureDate  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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<b>Application / Conf. No.</b>	10/805,116 / 8509
<b>Filing Date</b>	March 19, 2004
<b>First Named Inventor</b>	Eric J. Crabill
<b>Art Unit</b>	2819
<b>Examiner Name</b>	Unknown
<b>Attorney Docket Number</b>	X-841-1C US

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**OCT 25 2004**

PTO/SB/08A (10-01)

Approved for use through 10/31/2002 OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	3	of	4
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## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

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**Examiner  
Signature**

Date  
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		<b>First Named Inventor</b>	Eric J. Crabill
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		<b>Attorney Docket Number</b>	X-841-1C US
<b>Sheet</b>	4	<b>of</b>	4

OTHER – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		IBM; Technical Disclosure Bulletin; "System Master Slice for Fast Turnaround Time"; Vol. 26, No. 3B; August 1983; pp. 1531-1532.	
		IBM; Technical Disclosure Bulletin; "Update Mechanism for Personal Computer System Resident Firmware"; Vol. 34, No. 10B; March 1992; pp. 133-136.	
		IBM; Technical Disclosure Bulletin; "Protocol Extensions to Microprocessor Memory Bus to Support Extend Extended Address Space"; Vol. 37, No. 05; May 1994; pp. 389.	
		IBM; Technical Disclosure Bulletin; "Efficient Mechanism for Multiple Debug Modes"; Vol. 38, NO. 11; November 1995; pp. 65-68.	

<b>Examiner Signature</b>		<b>Date Considered</b>	
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